# **High Voltage Transistor**

# **PNP Silicon**

#### Features

• Pb-Free Package is Available\*

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>CEO</sub>	-350	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	-350	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	-6.0	Vdc
Collector Current – Continuous	Ι <sub>C</sub>	-500	mAdc
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above = $25^{\circ}C$	P <sub>D</sub>	625 5.0	mW mW/°C
Total Device Dissipation @ $T_C = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

#### THERMAL CHARACTERISTICS

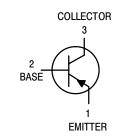
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\thetaJA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



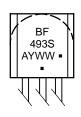
### **ON Semiconductor®**

http://onsemi.com





#### MARKING DIAGRAM



BF493S = Device Code А

= Assembly Location

= Year

Υ

WW

= Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

Device	Package	Shipping
BF493S	TO-92	5000 Units / Box
BF493SG	TO–92 (Pb–Free)	5000 Units / Box

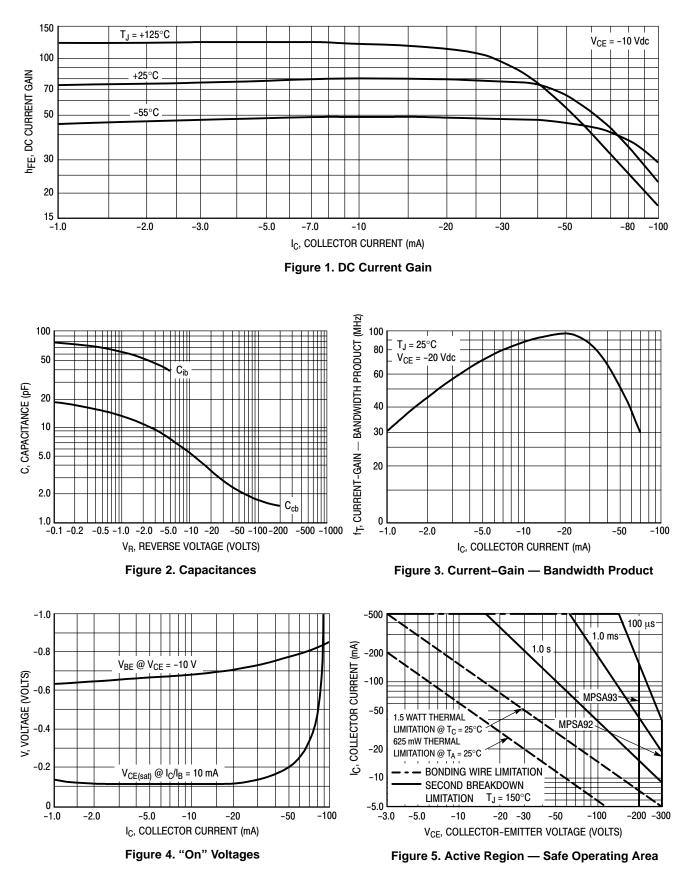
\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# **BF493S**

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = $25^{\circ}$ C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage (Note 1) $(I_C = -1.0 \text{ mAdc}, I_B = 0)$	V <sub>(BR)CEO</sub>	-350	-	Vdc
Collector – Base Breakdown Voltage $(I_C = -100 \ \mu Adc, I_E = 0)$	V <sub>(BR)CBO</sub>	-350	-	Vdc
Emitter – Base Breakdown Voltage ( $I_E = -100 \ \mu Adc, I_C = 0$ )	V <sub>(BR)EBO</sub>	-6.0	-	Vdc
Collector Cutoff Current (V <sub>CE</sub> = -250 Vdc)	ICES	-	-10	nAdc
Emitter Cutoff Current ( $V_{EB} = -6.0 \text{ Vdc}, I_C = 0$ )	I <sub>EBO</sub>	-	0.1	μAdc
Collector Cutoff Current $(V_{CB} = -250 \text{ Vdc}, I_E = 0, T_A = 25^{\circ}\text{C})$ $(V_{CB} = -250 \text{ Vdc}, I_E = 0, T_A = 100^{\circ}\text{C})$	I <sub>CBO</sub>		-0.005 -1.0	μAdc
ON CHARACTERISTICS		1		
DC Current Gain (I <sub>C</sub> = -1.0 mAdc, V <sub>CE</sub> = -10 Vdc) (I <sub>C</sub> = -10 mAdc, V <sub>CE</sub> = -10 Vdc)	h <sub>FE</sub>	25 40		-
Collector – Emitter Saturation Voltage ( $I_C = -20 \text{ mAdc}, I_B = -2.0 \text{ mAdc}$ )	V <sub>CE(sat)</sub>	-	-2.0	Vdc
Base – Emitter On Voltage ( $I_C = -20 \text{ mA}, I_B = -2.0 \text{ mA}$ )	V <sub>BE(sat)</sub>	-	-2.0	Vdc
DYNAMIC CHARACTERISTICS		1		
Current-Gain – Bandwidth Product (I <sub>C</sub> = -10 mAdc, V <sub>CE</sub> = -20 Vdc, f = 20 MHz)	f <sub>T</sub>	50	-	MHz
Common–Emitter Feedback Capacitance ( $V_{CB} = -100 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz}$ )	C <sub>re</sub>	-	1.6	pF
Dulas Test: Dulas Width < 200 us: Duty Cycle < 2.0%	1	1		1

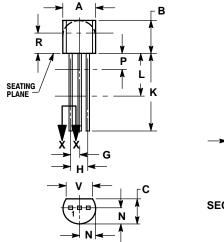
1. Pulse Test: Pulse Width  $\leq$  300 µs; Duty Cycle  $\leq$  2.0%.



#### **BF493S**

#### PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AL** 





NOTES 1.

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- 2.
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 3.
- LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM. 4

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Η	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
Κ	0.500		12.70	
Г	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.115		2.93	
۷	0.135		3.43	
style Pii		TTER		

BASE
COLLECTOR

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